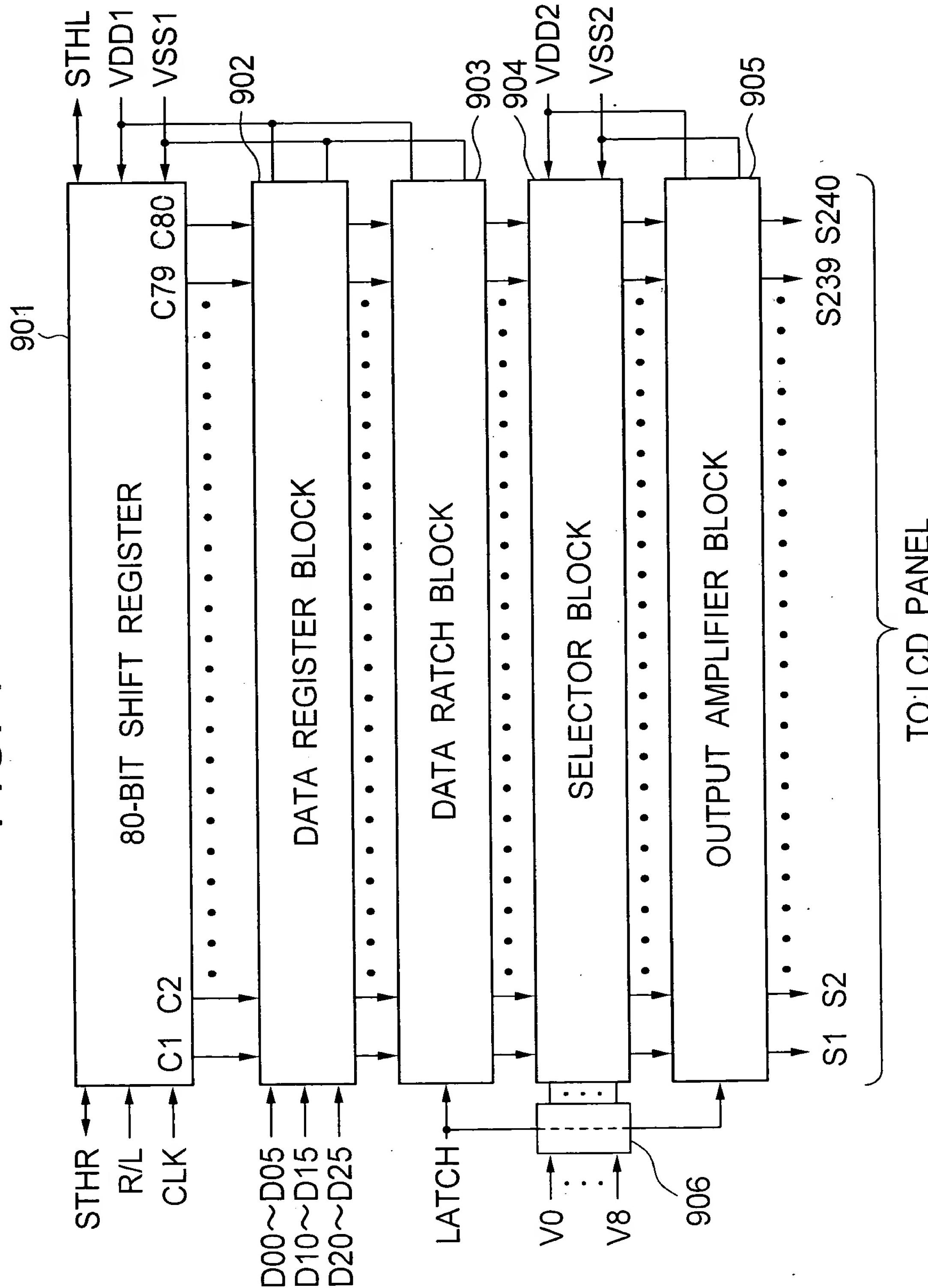
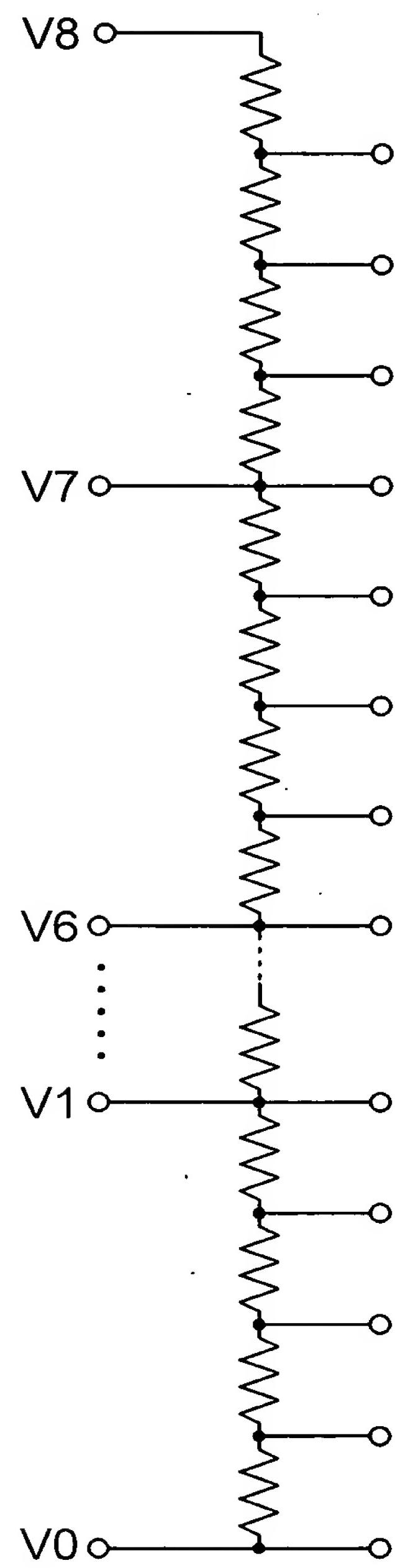


**6570560**

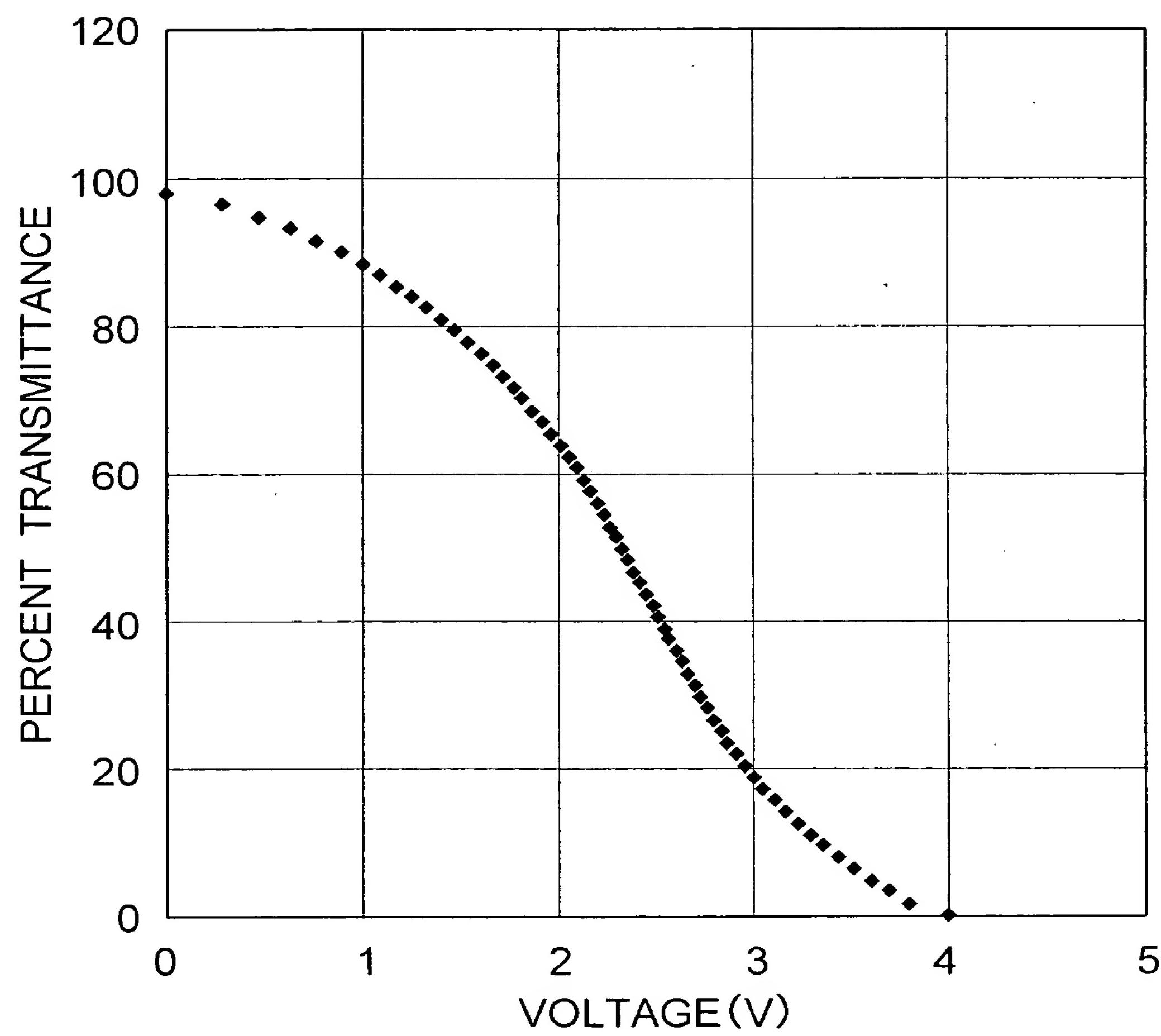
**FIG. 1** PRIOR ART



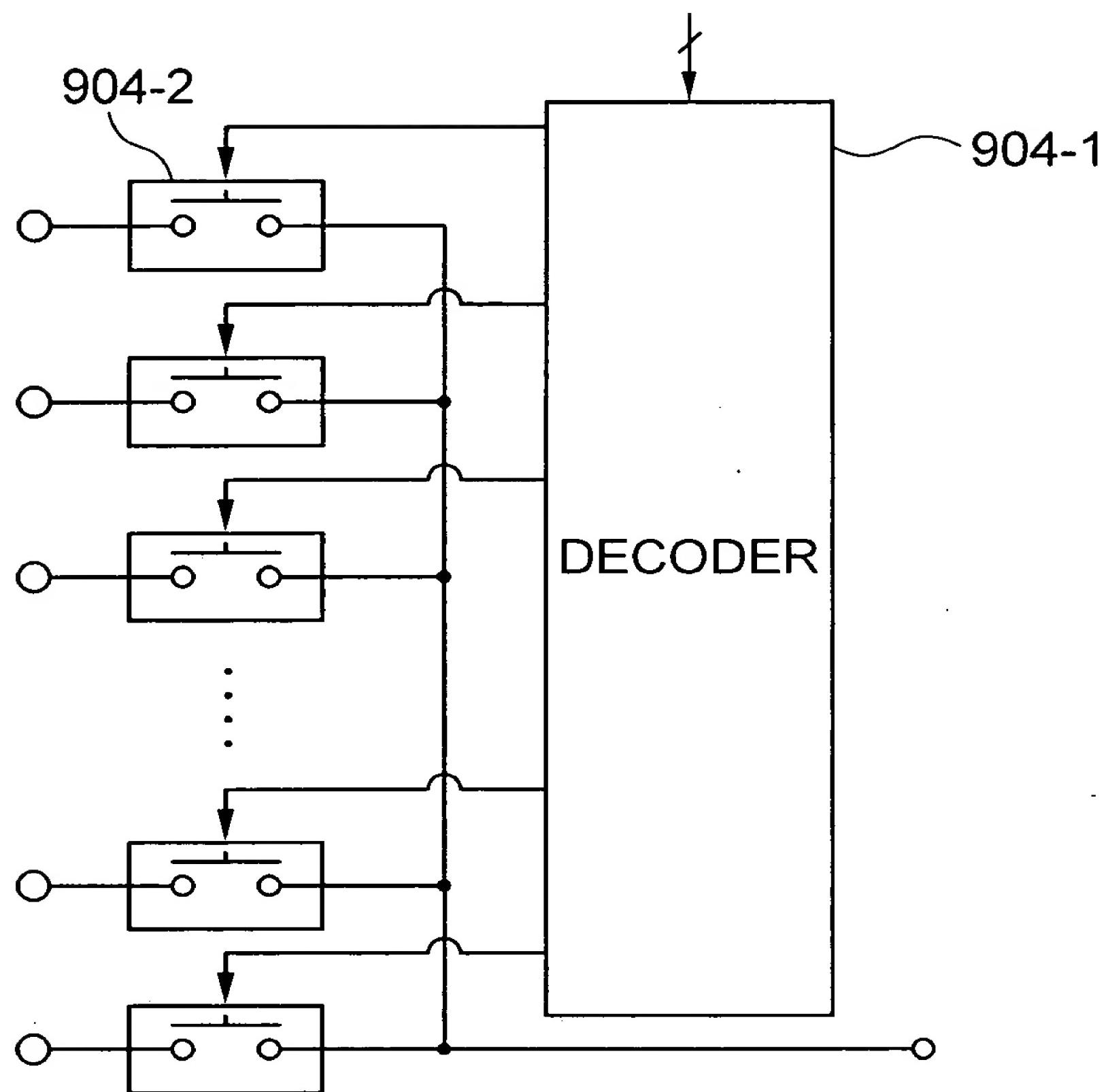
**FIG. 2** PRIOR ART



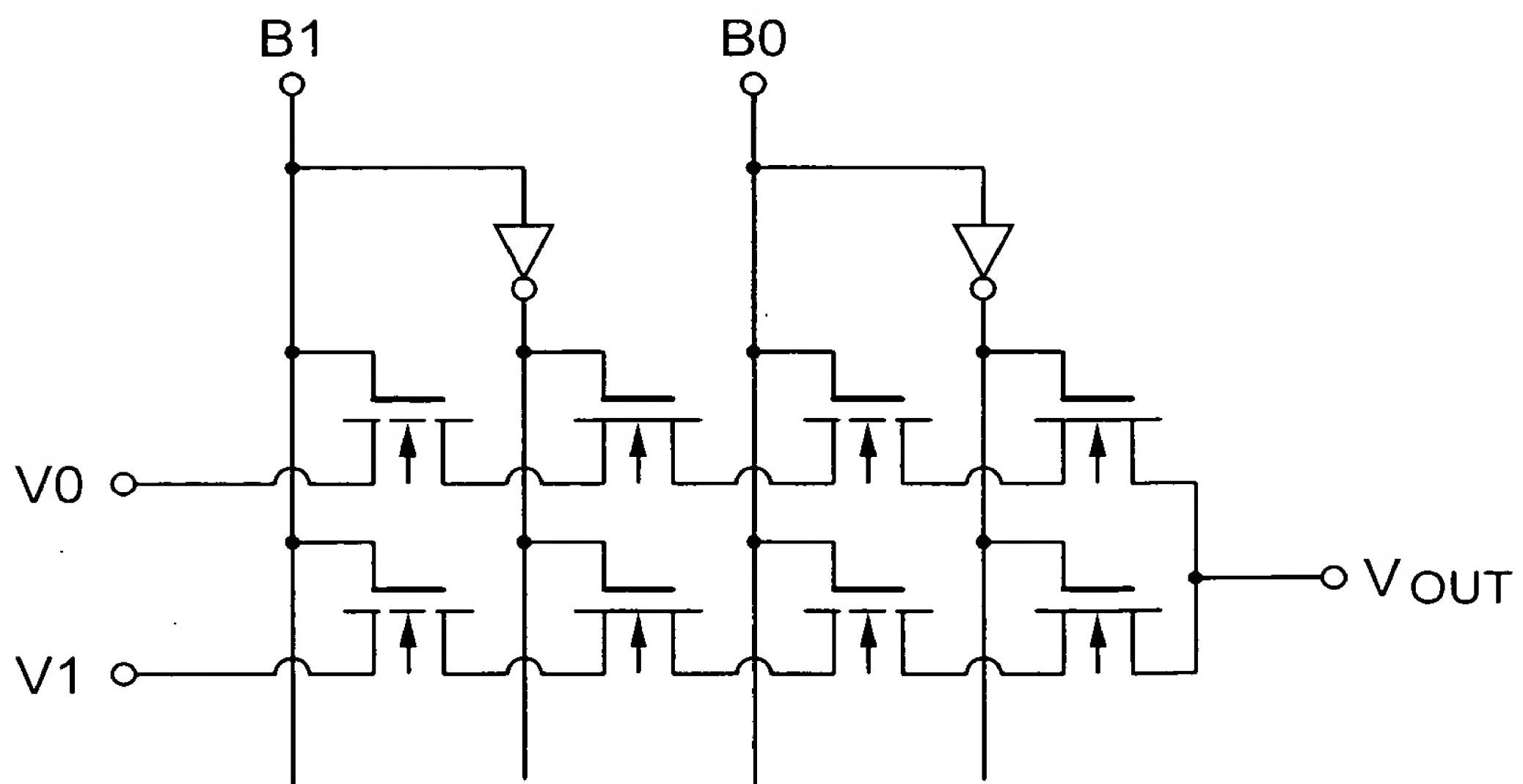
**FIG. 3 PRIOR ART**



**FIG. 4 PRIOR ART**



**FIG. 5 PRIOR ART**



**FIG. 6**

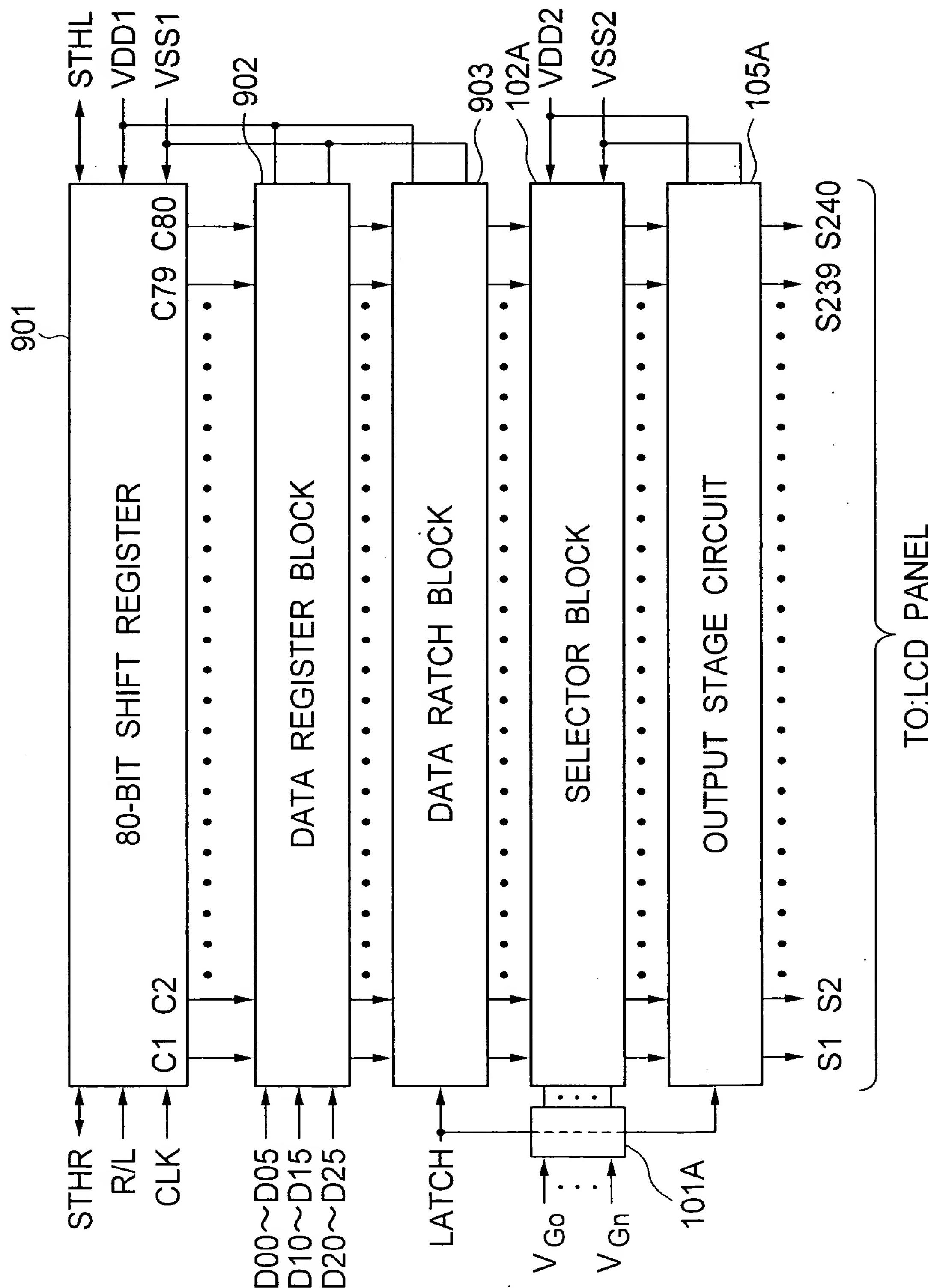
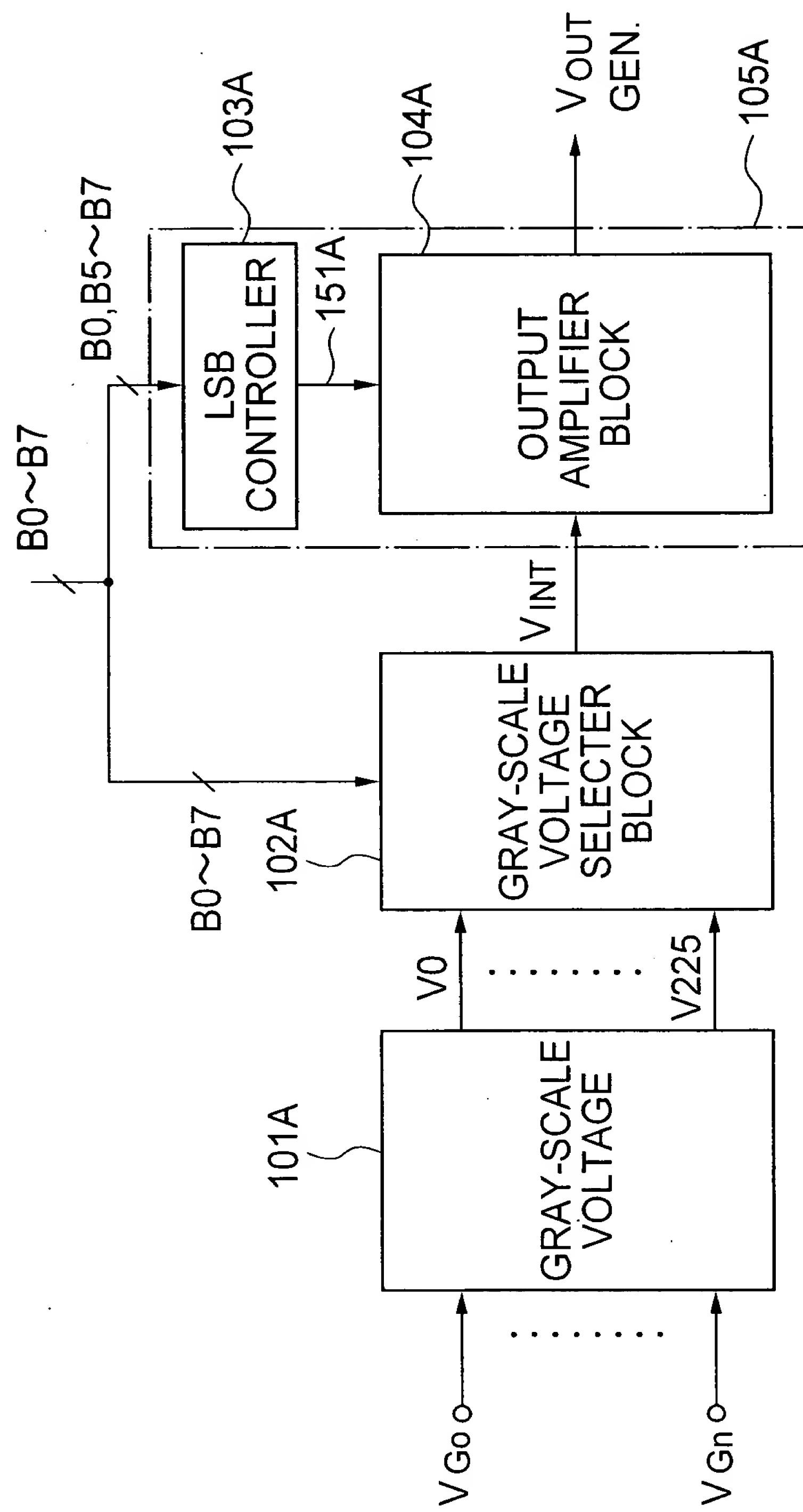


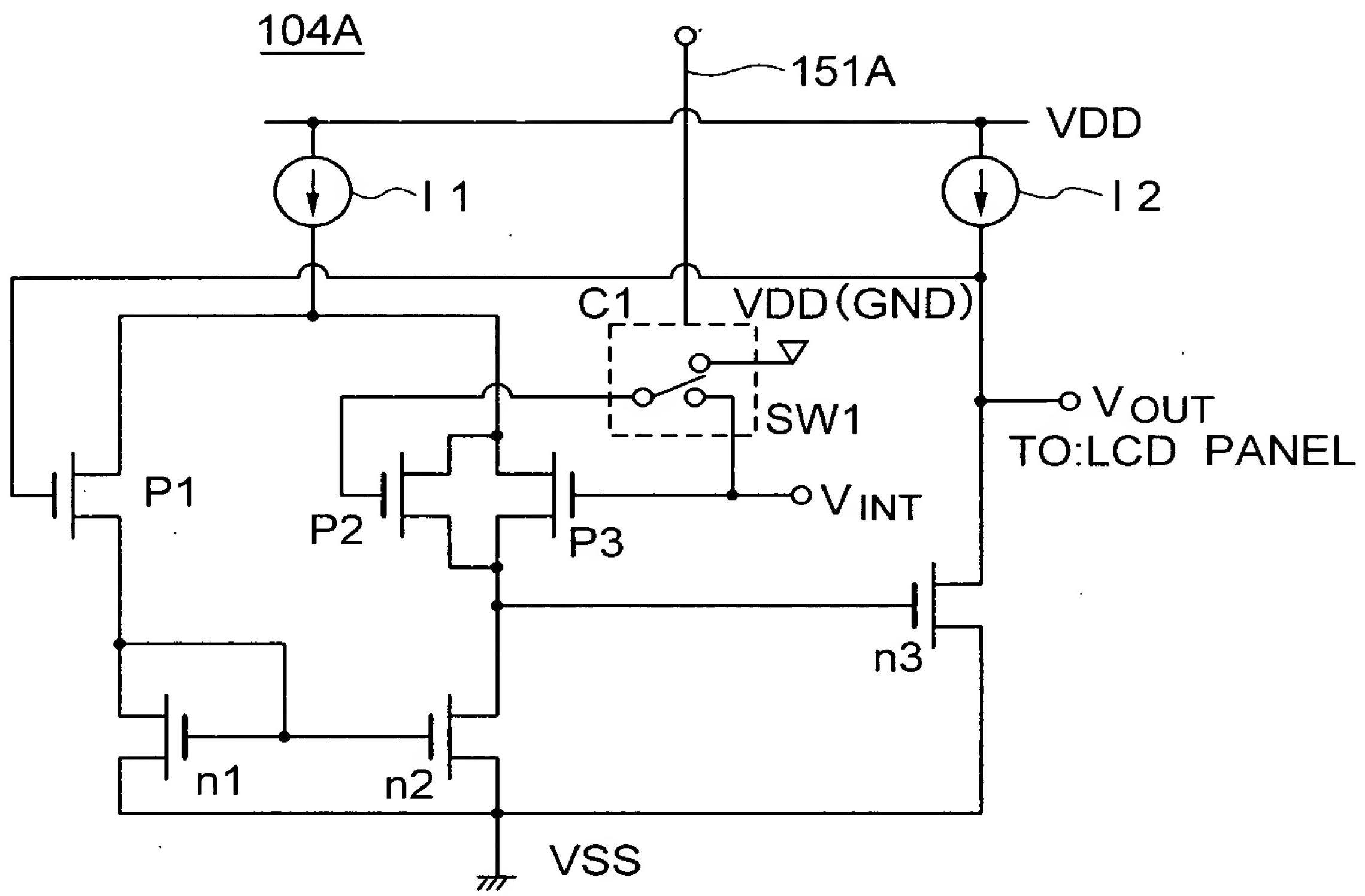
FIG. 7



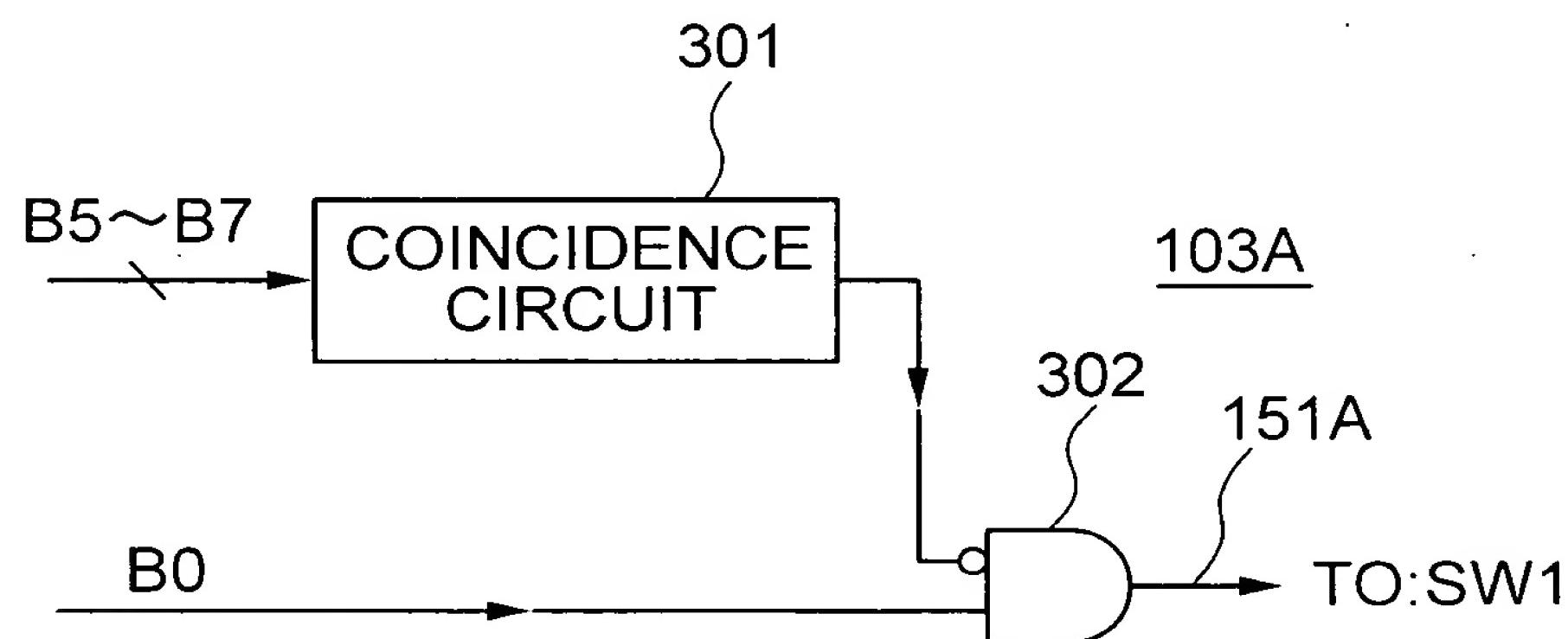
# FIG. 8

VIDEO DATA	V OUT
00000000	V0
00000001	V1
00000010	V2
00000011	V3
:	
:	
:	
00011110	V30
00011111	V31
-----	
00100000	V32
00100001	V32+ $\alpha$
00100010	V34
00100011	V34+ $\alpha$
:	
:	
:	
11011110	V222
11011111	V222+ $\alpha$
-----	
11100000	V224
11100001	V225
:	
:	
:	
11111110	V254
11111111	V255

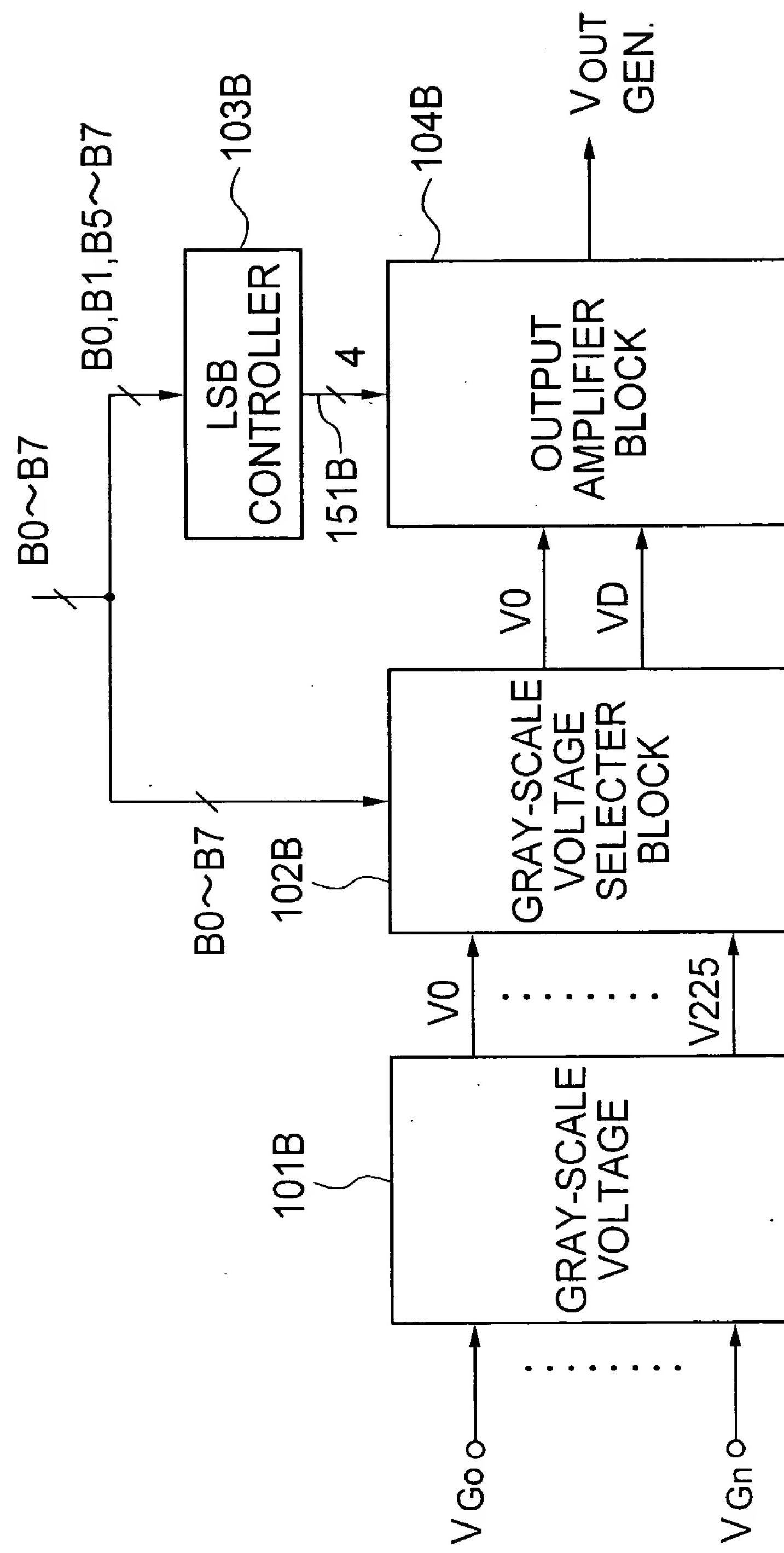
**FIG. 9**



**FIG. 10**



**FIG. 11**



# FIG. 12

VIDEO DATA	V OUT
00000000	V0
00000001	V1
00000010	V2
00000011	V3
:	
:	
:	
00011110	V30
00011111	V31
<hr/>	
00100000	V32
00100001	(3/4)V32+(1/4)V36
00100010	(2/4)V32+(2/4)V36
00100011	(1/4)V32+(3/4)V36
00100100	V36
:	
:	
:	
11011100	V220
11011101	(3/4)V220+(1/4)V224
11011110	(2/4)V220+(2/4)V224
11101111	(1/4)V220+(3/4)V224
<hr/>	
11100000	V224
11100001	V225
:	
:	
:	
11111110	V254
11111111	V255

FIG. 13

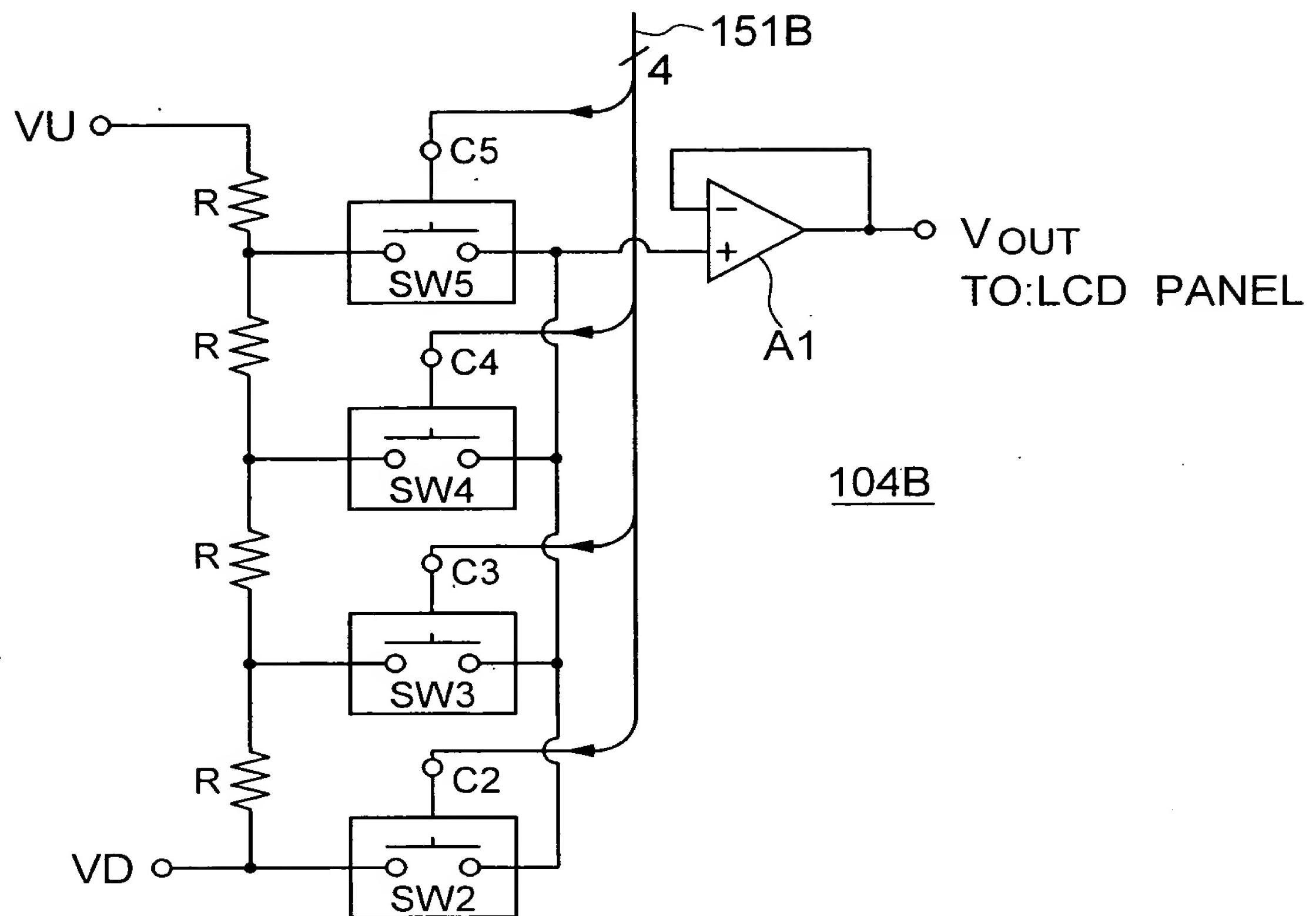


FIG. 14

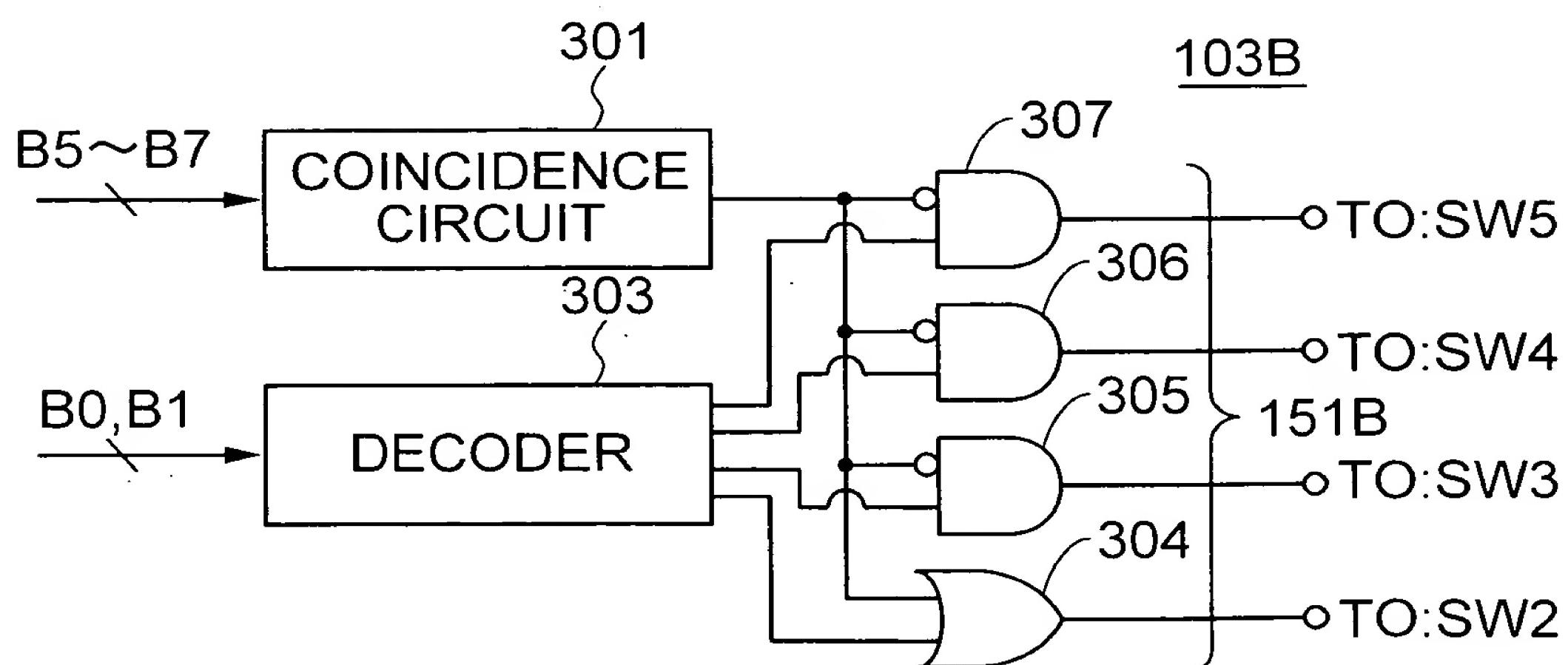


FIG. 15

